



N THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Number	10/632224	Docket Number	CNTR.2141		
Filed	7/31/2003	Group Art Unit	2183		
Examiner	EDDIE CHAN	Customer No.	23669		
Application Title	APPARATUS AND METHOD FOR KILLING AN INSTRUCTION AFTER LOADING THE INSTRUCTION INTO AN INSTRUCTION QUEUE IN A PIPELINED MICROPROCESSOR				
First Named Inventor	THOMAS C. MCDONALD				

INFORMATION DISCLOSURE STATEMENT TRANSMITTAL

Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Attached hereto is Form PTO-1449 listing documents believed relevant to the subject application. It is respectfully requested that the examiner review the information disclosed herein in detail, independently evaluate each item carefully in the consideration of the pending claims, and return an initialed copy of each form to the undersigned practitioner.

This disclosure statement should not be construed as a representation that a search has been made, that no other material information as defined in 37 CFR 1.56(a) exists, or as an admission that the information cited in the statement is, or is considered to be, material to patentability as defined in 37 CFR 1.56(b) or is available as a reference under 35 U.S.C. §102 et seq. Applicant reserves the right to swear behind or otherwise disprove the allege "prior" nature of any art cited should the facts support and the situation warrant such an action.

It is believed that this disclosure complies with the requirements of 37 CFR 1.56, 1.97, and 1.98, and the Manual of Patent Examining Procedures §609. If for some reason the examiner considers otherwise, it is respectfully requested that the undersigned be called so that any deficiencies can be remedied.

Some of the enclosed documents may have markings thereon. No significance is intended to be attached to the markings.

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Alexandria, VA 22313-1450.	Tayou Locke				
Respectfully submitted,					
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Date 7-9-05



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INFORMATION DISCLOSURE

STATEMENT BY APPLICANT

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First Named Inventor	THOMAS C. MCDONALD
Group Art Unit	2183
Examiner Name	EDDIE CHAN
Attorney Docket Number	CNTR.2141

			U.S. PATEN	T DOCUMENTS	
Examiner Initials*	Cite No.1	Document Number Number-Kind Code ^{2(if known)}	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		5706491	01-06-1998	Steven C. McMahan	Арреаі
		5313634	05-17-1994	James Eickemeyer	
		5961629	10-05-1999	Nguyen et al	
		5832289	11-03-1998	Shaw et al	
		5828901	10-27-1998	O'Toole et al	
		5864707	01-26-1999	Tran et al	
		6035391	03-07-2000	David L. Isaman	
		5353421	10-04-1994	Emma et al	
		6081884	06-27-2000	Paul K. Miller	
		6250821	06-26-2001	Greg Schwendinger	
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		6233676	05-15-2001	Henry et al	
		5623615	04-22-1997	Salem et al	
		6175897	01-16-2001	Ryan et al	
		5734881	03-31-1998	White et al	
		6260138	07-10-2001	Jeremy G. Harris	
		6823444	11-23-2004	Henry et al	
		6256727	07-03-2001	Robert Greg McDonald	
		5931944	08-03-1999	Ginosar et al	
		5881260	03-09-1999	Raje et al	
		4860197	08-22-1989	Langendorf et al	
		5142634	08-25-1992	Fite et al	
		5163140	11-10-1992	Stiles et al	
		5404467	04-04-1995	Saba et al	

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Substitute for form 1445/VI	-10			Application Number	10/632224
INFORM	ATION DISC	LOS	JRE	Filing Date	7/31/2003
STATEMENT BY APPLICANT				First Named Inventor	THOMAS C. MCDONALD
				Group Art Unit	2183
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Sheet	2	of	4	Attorney Docket Number	CNTR.2141

U.S. PATENT DOCUMENTS						
Examiner	Cite	Document Number	Publication Date	Name of Patentee or	Pages, Columns, Lines, Where	
Initials*	No. ¹	Number-Kind Code ^{2(if known)}	MM-DD-YYYY	Applicant of Cited Document	Relevant Passages or Relevant Figures Appear	
		5687360	11-11-1997	Po-Hua Chang		
		5761723	06-02-1998	Black et al		
		5867701	02-02-1999	Brown et al		
		5964868	10-12-1999	Gochman et al		
		5974543	10-26-1999	Hilgendorf et al		
		5978909	11-02-1999	Oded Lempel		
·		6088793	07-11-2000	Liu et al		
		6151671	11-21-2000	D'Sa et al		
		6314514	11-06-2001	Thomas C. McDonald		
		6502185	12-31-2002	Keller et al		
		6601161	07-29-2003	Rappoport et al		
		6647467	11-11-2003	Eric M. Dowling		
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		5850543	12-15-1998	Shiell et al		
		6108773	08-22-2000	Col et al		
		5513330	04-30-1996	David R. Stiles		
		6457120	09-24-2002	Balaram Sinharoy		
		5634103	05-27-1997	Dietz et al		
		6044459	03-28-2000	Bae et al		
		5530825	06-25-1996	Black et al		
_		5812839	09-22-1998	Hoyt et al		
		5604877	02-18-1997	Hoyt et al		
		6134654	10-17-2000	Patel et al		
		4181942	01-01-1980	Forster et al		
		5687349	11-11-1997	Ralph C. McGarity		
		6279105	08-21-2001	Konigsburg et al		

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Initials*	No. ¹	Number-Kind Code ^{2(if known)}	MM-DD-YYYY	Applicant of Cited Document	Appear		
		5968169	10-19-1999	James K. Pickett			
		6085311	07-04-2000	Narayan et al			
		6308259	10-23-2001	David B. Witt			
		5623614	04-22-1997	Van Dyke et al			
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		5721855	02-24-1998	Hinton et al			
-		4200927	04-29-1980	Hughes et al			
		6185676	02-06-2001	Poplingher et al.			
		5394530	02-28-1995	Mayumi Kitta			
		6886093	04-26-2005	G. Glenn Henry			
		20040030866	02-12-2004	Thomas C. McDonald			
		20020194461	12-19-2002	Henry et al.			
		20020194460	12-19-2002	Henry et al.			
-		20050132175	06-16-2004	Henry et al.			
		20050044343	02-24-2005	Henry et al.			
		20020188833	12-12-2002	Henry et al.			
		20050114636	05-26-2005	McDonald et al.			
		20040139281	07-15-2004	Thomas C. McDonald			
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		20040143709	07-22-2004	Thomas C. McDonald			
4		20040139292	07-15-2004	Thomas C. McDonald			
		20050076193	04-07-2005	Henry et al.			

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				Group Art Unit	2183	
				Examiner Name	EDDIE CHAN	
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NON PATENT LITERATURE DOCUMENTS						
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²			
AA		Microprocessor Report. Vol.9. No.2. February 16, 1995. page 5				
	AB .	Microprocessor Report. August 23, 1999. page 7				
	AC	YEH ET AL. Alternative Implementation of Two-Level Adaptive Branch Prediction. 19th Annual International Symposium on Computer Architecture. Pp124-134. May 19-21, 1992. Gold Coast, Australia				
	AD .	CHANG ET AL. Alternative Implementations of Hybrid Branch Predictors. Proceedings of MICRO-28. 1995. IEEE.				
	AE	MC FARLING, SCOTT. WRL Technical Note TN-36. "Combining Branch Predictors. June 1993. Western Research Laboratory, 250 University Ave., Palo Alto, CA 94301				
	AF,	BRAY ET AL. Strategies For Branch Target Buffers. Technical Report No. CSL-TR-91-480. June 1991.				
	AG	SAKAMOTO ET AL. Microarchitecture Support for Reducing Branch Penalty in a Superscaler Processor. Pp 208-216. Mittsubishi Electric Corp. 4-1 Mizuhara, Itami, Hyogo 664. Japan, 1996. IEEE.				
	АН	IEEE 100, The Authoritative Dictionary of IEEE Standard Terms, Seventh Edition. The Institute of Electrical Engineering, Inc. New York: Standards Information Network IEEE Press Page 135.				

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